

Substitute for form 1449B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/781,334
				Filing Date	February 17, 2004
				First Named Inventor	Cameron McClintock
				Art Unit	2819
				Examiner Name	Don P. Le
Sheet	4	of	4	Attorney Docket Number	015114-052530US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>lw</i>	77	UEDA et al. "A 3.3V ASIC for Mixed Voltage Applications With Shut Down Mode," <i>IEEE 1993 Custom Integrated Circuits Conference</i> , May 9-12, 1993, pp. 25.5.1 to 25.5.4.	
	78	VOLDMAN "ESD Protections in a Mixed Voltage Interface and Multi-Rail Disconnected Power Grid Environment in 0.50- and 0.25- μ m Channel Length CMOS Technologies," <i>EOS/ESD Symposium</i> , 1994, pp. 3.4.1 to 3.4.10.	
	79	VU et al. "A Gallium Arsenide SDFL Gate Array with On-Chip RAM," <i>IEEE Journal of Solid-State Circuits</i> , Vol. sc-19, No. 1, February 1984, pp. 10-22.	
<i>lw</i>	80	WILLIAMS "Mixing 3-V and 5-V ICs," <i>IEEE Spectrum</i> , March 1993, pp. 40-42.	

Examiner Signature	<i>DON</i>	<i>W</i>	Date Considered	<i>8/26/04</i>
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WL	61	Altera Corporation, Data Sheet, "Flex 10K Embedded Programmable Logic Family," Jul., 1995, ver. 1, pp. 1-39.		
	62	Altera Corporation, Data Sheet, "Apex 20K", May, 1999, pp. 23-88.		
	63	CHAPPELL et al. "Fast CMOS ECL Receivers With 100-mV Worst-Case Sensitivity," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 23, No. 1, February 1988, pp. 59-66.		
	64	CLAUDE "Cross-boundary PLDs," <i>Semiconductor Currents</i> , June 1991, pp. 9-10.		
	65	GUARDIANI et al. "Applying a submicron mismatch model to practical IC design," <i>IEEE 1994 Custom Integrated Circuits Conference</i> , 1994, pp. 297-300.		
	66	GUNNING et al., "A CMOS Low-Voltage-Swing Transmission-Line Transceiver," <i>IEEE International Solid-State Circuits Conference</i> , 1992, pp. 58-59.		
	67	HAINES "Field-programmable gate array with non-volatile configuration," <i>Microprocessors and Microsystems</i> , Vol. 13, No. 5, June 1989, pp. 305-312.		
	68	HANAFI et al. "Design and Characterization of CMOS Off-Chip Driver/Receiver with Reduced Power-Supply Disturbance," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 27, No. 5, may 1992, pp. 783-785.		
	69	MARTINEZ "IEEE 1194.1 BTL-Enabling Technology for High Speed Bus Applications," National Semiconductor Application Note 829, June 1992, pp. 1-5.		
	70	KNACK "Debunking High-Speed PCB Design Myths," <i>ASIC & EDA</i> , July 1993, pp. 12-26.		
	71	PELGROM et al. "A 3/5 V Compatible I/O Buffer," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 30, No. 7, July 1995, pp. 823-825.		
	72	PELGROM et al. "Matching Properties of MOS Transistors," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 24, No. 5, October 1989, pp. 1433-1440.		
	73	PRINCE et al. "ICs going on a 3-V diet," <i>IEEE Spectrum</i> , May 1992, pp. 23-25.		
	74	ROBERTS et al. "Session XIX: High Density SRAMs," <i>IEEE International Solid-State Circuits Conference</i> , February 27, 1987, pp. 252-254.		
	75	SENTHINATHAN et al., "Application Specific CMOS Output Driver Circuit Design Techniques to Reduce Simultaneous Switching Noise," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 28, No. 12, December 1993, pp. 1383-1388.		
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U.S. PATENT DOCUMENTS+					
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
W	42	US-4,783,607		11-08-1988	Hsieh
	43	US-4,742,252		05-03-1988	Agrawal
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W	48	US-4,032,800		08-28-1977	Droscher et al.

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		Country Code ³	Number ⁴		
W	49	EP	0 358 501	A2	14-03-1990
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	56	JP	04-223617		Ricoh
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	59	JP	01-274512		Hitachi
W	60	PCT	WO98/47230		Xilinx, Inc.

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	1	US-6,335,637	01-01-2002	Correale, Jr. et al.	
	2	US-6,229,385	05-08-2001	Iketani et al.	
	3	US-6,049,227	04-11-2000	Goetting et al.	
	4	US-6,040,712	03-21-2000	Mejia	
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